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Bib Data Sheet

CONFIRMATION NO. 5137

SERIAL NUMBER 10/501,651	FILING DATE 01/10/2005 RULE	CLASS 361	GROUP ART UNIT 2836	ATTORNEY DOCKET NO. UC1.PAU.05US
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APPLICANTS

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**** CONTINUING DATA *******
 This application is a 371 of PCT/US03/01420 01/16/2003
 which claims benefit of 60/349,899 01/18/2002

**** FOREIGN APPLICATIONS *******

**** SMALL ENTITY ****

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY CA	SHEETS DRAWING 13	TOTAL CLAIMS 30	INDEPENDENT CLAIMS 7
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Verified and Acknowledged
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TITLE
 On-chip esd protection circuit for compound semiconductor heterojunction bipolar transistor rf circuits

FILING FEE RECEIVED 377	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue)
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